



FSMTC 18-749

DRIFT

Approved
TP
1/20/03

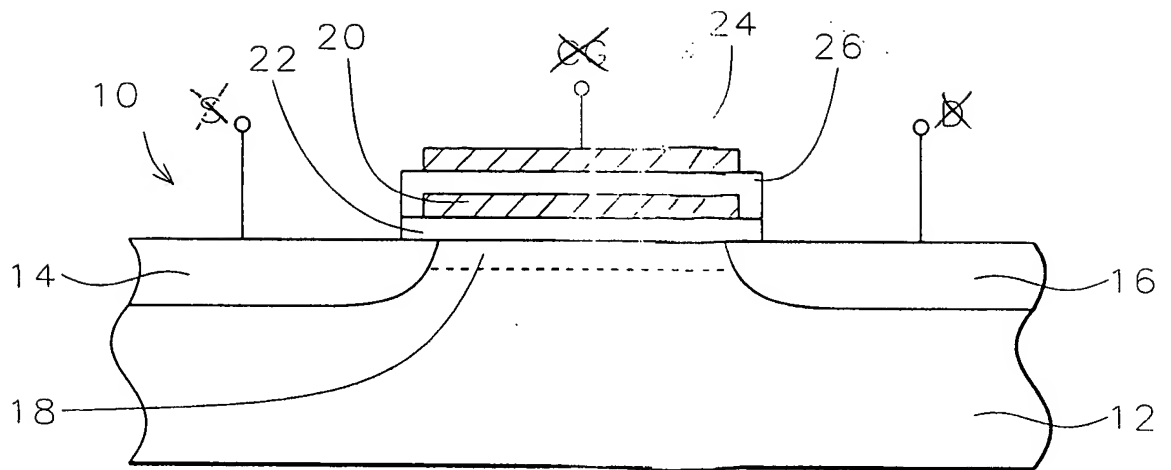


FIG. 1

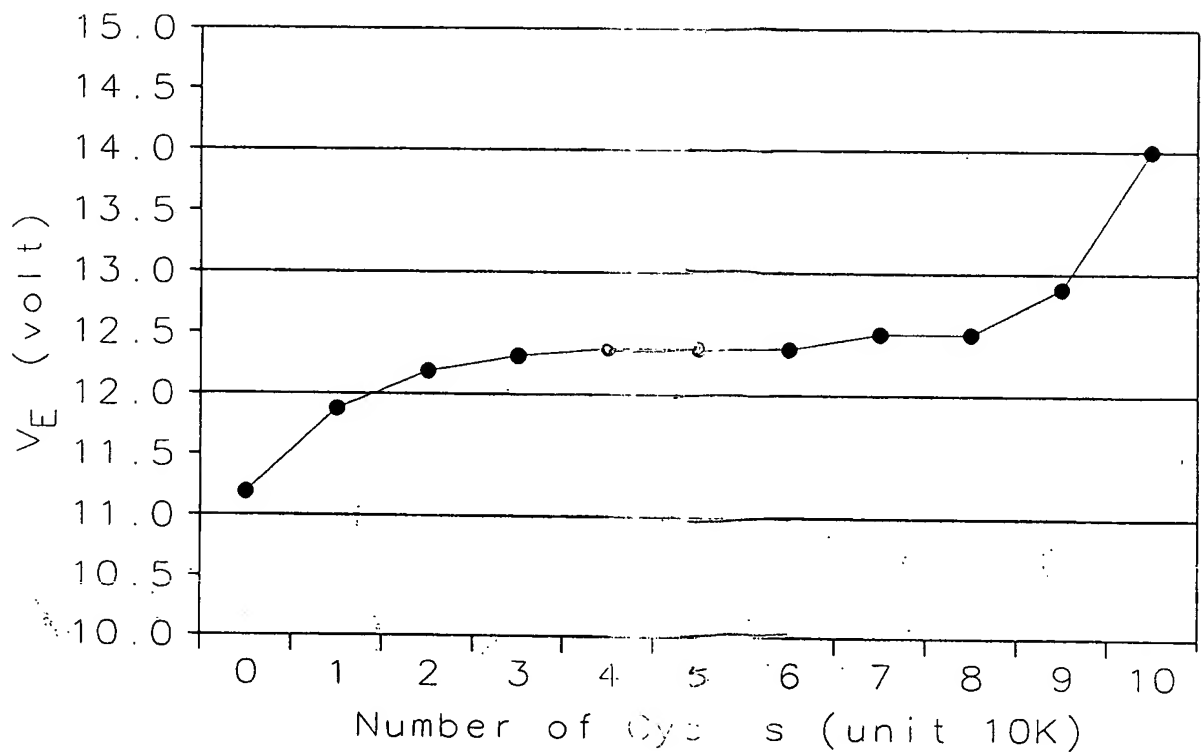


FIG. 2

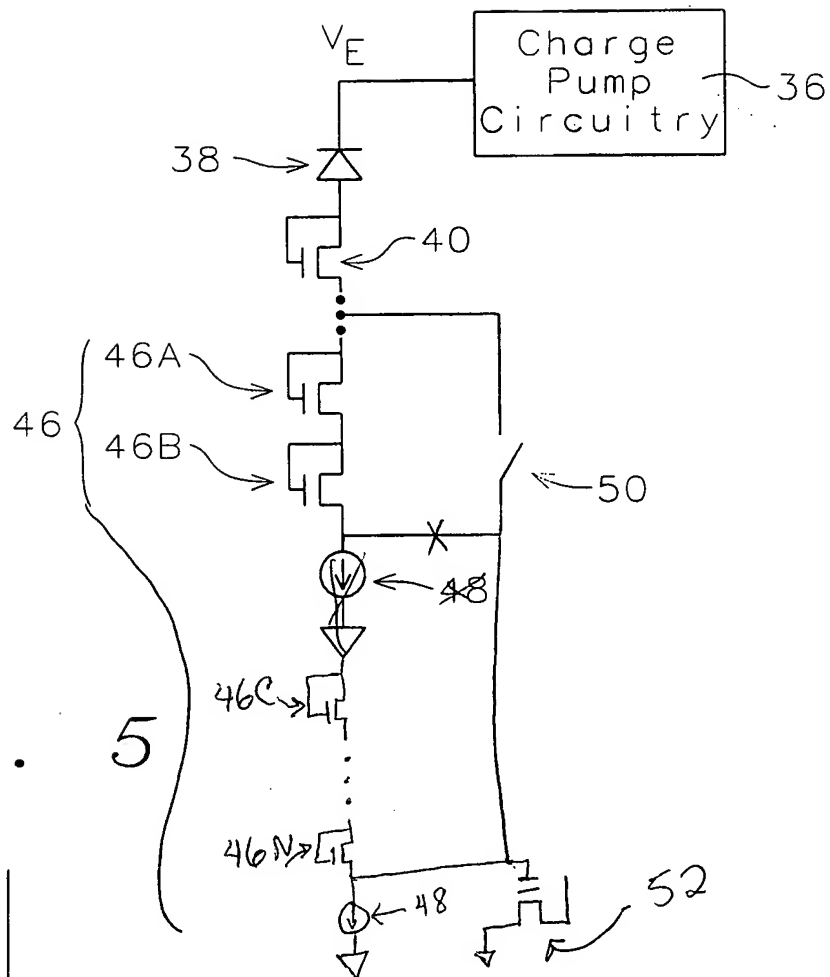
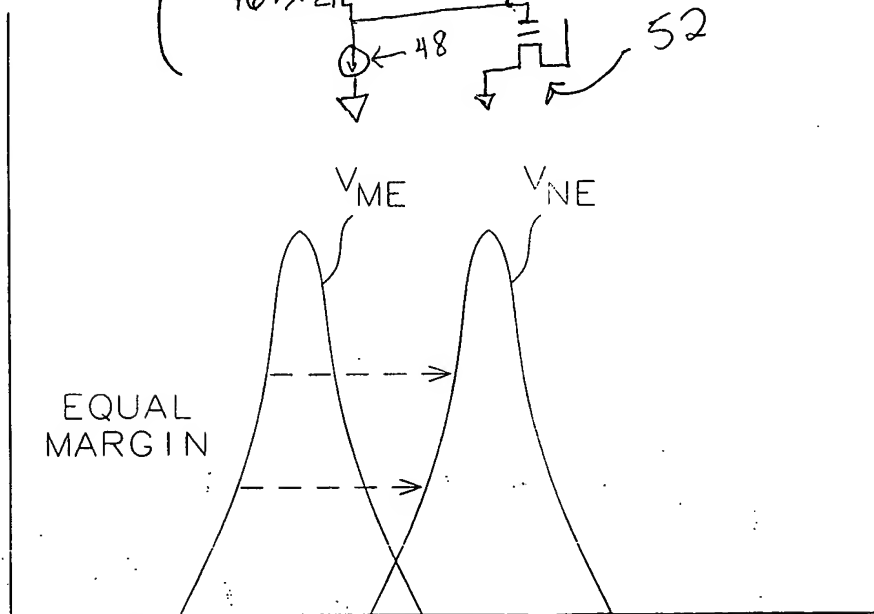


FIG. 5

Number of Samples



Chip Erase Voltage

FIG. 6